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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 5415

GOTOH et al.

Art Unit: 2857

Serial No.: 10/082,055

Examiner: D. Raymond

Filed: February 26, 2002

Docket No.: 100021-00074

For: TEST CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT
EFFECTIVELY CARRYING OUT VERIFICATION OF CONNECTION
OF NODES

AMENDMENT UNDER 37 C.F.R. § 1.111

Introductory Comments

Director of the U.S. PTO
P.O. Box 1450
Alexandria, Virginia 22313-1450

November 17, 2004

Sir:

In response to the Office Action dated May 17, 2004, the period for response being extended from September 17, 2004 until November 17, 2004 by the attached Petition for Extension of Time, please amend the above-titled application as follows: